

High-Efficiency Power Amplifier for 1.8 MHz

Sponsoring MTT-S Technical Committee

MTT-17 (HF/VHF/UHF Technology)

Coordinator

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Abstract

The student(s) will design an amplifier and driver to produce 10 W CW at 1.8 MHz from a 12-V supply. This design will use a single IRF510 power MOSFET for the final amplifier so that cost will not be an impediment to entry. The winning design will have the highest overall efficiency, subject to meeting the specifications below.

Notes

The resultant design should be useable on the 160-meter amateur band, and will produce about 50 W when operated from a higher supply voltage. (Not part of contest).

The low operating frequency necessitates the use of discrete components, making this design quite different from the usual 1+ GHz of the MTT-5 PA-design contest. Waveforms can be observed with an oscilloscope, and measurements can be made with relatively simple equipment. Inclusion of the driver presents the student with a number of interesting design trade-offs.

Detailed Competition Description and Rules

Output power: 10-11 W CW into a 50-ohm load. Only the fundamental-frequency component of the output will be counted toward the measured power.

Frequency: 1.8 MHz

Power Supply: 12 V (measured at the supply connectors). Final amp, driver, bias, etc. must all work from the single 12-V supply.

Final Amplifier: One IRF510.

Driver and Intermediate Stages: At discretion of student but must use discrete components as noted below.

Input from External Signal Source: +10 dBm (10 mW). CW, constant-amplitude sine wave, no modulation.

Input SWR: < 2:1 relative to 50 ohms.

Harmonics: < 40 dBc (up to 100 MHz)

Non-harmonic spurs: < -70 dBc (10 kHz to 100 MHz).

Oscillations: No oscillation if signal input is removed.

Subassemblies: No commercial subassemblies are allowed. For example, if a dc to dc converter is used, it must be designed and built with discrete parts and/or ICs, and included in the circuit schematic.

Batteries: Not permitted.

Connectors: SMA input and output for RF. Female banana jacks for the 12-V supply.

Winning Design:

- (a) Must meet all specs above.
- (b) Will have the highest overall efficiency defined as
$$\text{(RF output power)} / \text{(Total dc-input power)}$$
- (c) Provides the information specified by the due dates.
- (d) Presents working amplifier for judging at IMS.

The judges reserve the right to make multiple awards, or no awards in case of poor performance.

How to Participate

Needed by April 1, 2019

Potentially interested participants must submit registration information on the participants as soon as possible. This information includes the designated contact person and the following for each participant:

Name
Address
Phone
E-mail
Institution
Degree program and advisor

Needed by May 1, 2019

By May 1, the team must provide:

Updates if any on the team members

Description of the amplifier

Photo of the amplifier

Circuit

Parts list

Measured performance data

Submit your information as a single PDF to f.raab@ieee.org.

You may make improvements on your amplifier between this date and the judging.

At least one team member must register for and attend IMS 2018 and be present at the judging.

Judging will be done on Tuesday of IMS 2019 in the exhibit area. Please check the ims2019.org web site for the specific time and place. We may also give you a time slot.

We will provide a signal generator, power supply, and measurement equipment.

During the judging, you will set-up and check your hardware. You may re-adjust before we take the official measurements.

You may bring spare parts and tools to use in case there is a problem.

Student Eligibility Criteria

Teams of one to four full-time students.

The work must be entirely by the named students without assistance from others.